

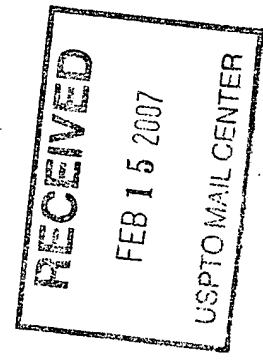
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In re Application of

Eric Hung, Geeta K. Desai, Vijendra Kuroodi, Alexander Miretsky, and Mirko Vojnovic
Application No. 10/728,492

Filed: December 5, 2003

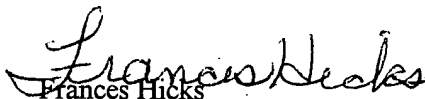
For: LOW POWER MEMORY CONTROLLER WITH LEADED DOUBLE DATA RATE DRAM PACKAGE
ARRANGED ON A TWO LAYER PRINTED CIRCUIT BOARD

Dear Mr. Alexander Miretsky:

You are named as a joint inventor in the above-identified United States patent application, filed under the provisions of 35 U.S.C. 116 (United States Code), and 37 CFR 1.47(a), Rules of Practice in Patent Cases. Should a patent be granted on the application you will be designated therein as a joint inventor.

As a named inventor you are entitled to inspect any paper in the file wrapper of the application, order copies of all or any part thereof (at a prepaid cost per 37 CFR 1.19) or make your position of record in the application. Alternatively, you may arrange to do any of the preceding through a registered patent attorney or agent presenting written authorization from you. If you care to join the application, counsel of record (see below) would presumably assist you. Joining the application would entail the filing of an appropriate oath or declaration by you pursuant to 37 CFR 1.63.

Telephone inquiries regarding this communication should be directed to Amelia Au at (571) 272-7414. Requests for information regarding your application should be directed to the File Information Unit at (703) 308-2733. Information regarding how to pay for and order a copy of the application, or a specific paper in the application, should be directed to the Certification Division at (571) 272-3150 or 1 (800) 972-6382 (outside the Washington, DC area).


Frances Hicks

Petitions Examiner
Office of Petitions

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cc: Leo Peters
LSI Logic Corporation
MS D-106
1621 Barber Lane
Milpitas CA 95035